

# WIP201610S-3R3ML



## Appearance & Shape



Series	L [mm]	W [mm]	W1 [mm]	T [mm]
201610	2.0±0.2	1.6±0.2	1.4±0.2	1.0 max.
201612	2.0±0.2	1.6±0.2	1.4±0.2	1.2 max.
252010	2.5±0.2	2.0±0.2	1.8±0.2	1.0 max.
252012	2.5±0.2	2.0±0.2	1.8±0.2	1.2 max.



注意 NOTICE: All Specifications are subject to change without notice.

1. 本文件由 INPAQ 佳邦科技網站下載，若規格變更及停產，請於訂購時確認，恕不另行通知。
2. 本文件僅說明參考標準規格，如需訂購，請依實際核准所需規格進行申辦。

## Specification

L size(mm)	2.0±0.2
W size(mm)	1.6±0.2
T size(mm)	1.0 max.
Size code_Inch (mm)	0806/2016
Inductance(uH)	3.3
DCR(mΩ)Typ.	200
DCR(mΩ)Max.	220
Isat(A)Typ.	1.9
Isat(A)Max.	1.65
Irms(A)Typ.	1.8
Irms(A)Max.	1.5
QValue(Typ.)	30
SRF[MHz] Typ.	40
Withstand voltage(DC)	20V DC
Operating Temperature range(includingself-temperaturerising)°C	-40°C to +125°C

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## Notices

### Notes

1. Customized design is available, please contact us.
  2. All test referenced to 25°C ambient.
  3. Inductance tolerance +/- 20%.
  4. Inductance is measured with Agilent® LCR meter 4285A (or equivalent) at 1MHz/1V.
  5. DC resistance is measured with HIOKI® micro-ohm meter RM3542 or equivalent.
  6. Isat : DC current (A) that will cause Li to drop approximately 30%.
  7. Irms : DC current (A) that will cause an approximate  $\Delta T$  of 40°C.
  8. The rated current value is following either Isat (max.) or Irms (max), which value is lower one.
  9. The part temperature (ambient + temp rise) should not exceed 125°C under worst case operating conditions.
- PCB land pattern, trace size - thick, circuit design and proximity to other components are all the factors will affect the temperature performance of the device. Therefore, it should be approved in application conditions and end product.
10. Rated voltage:20V DC (max.)

## Packing

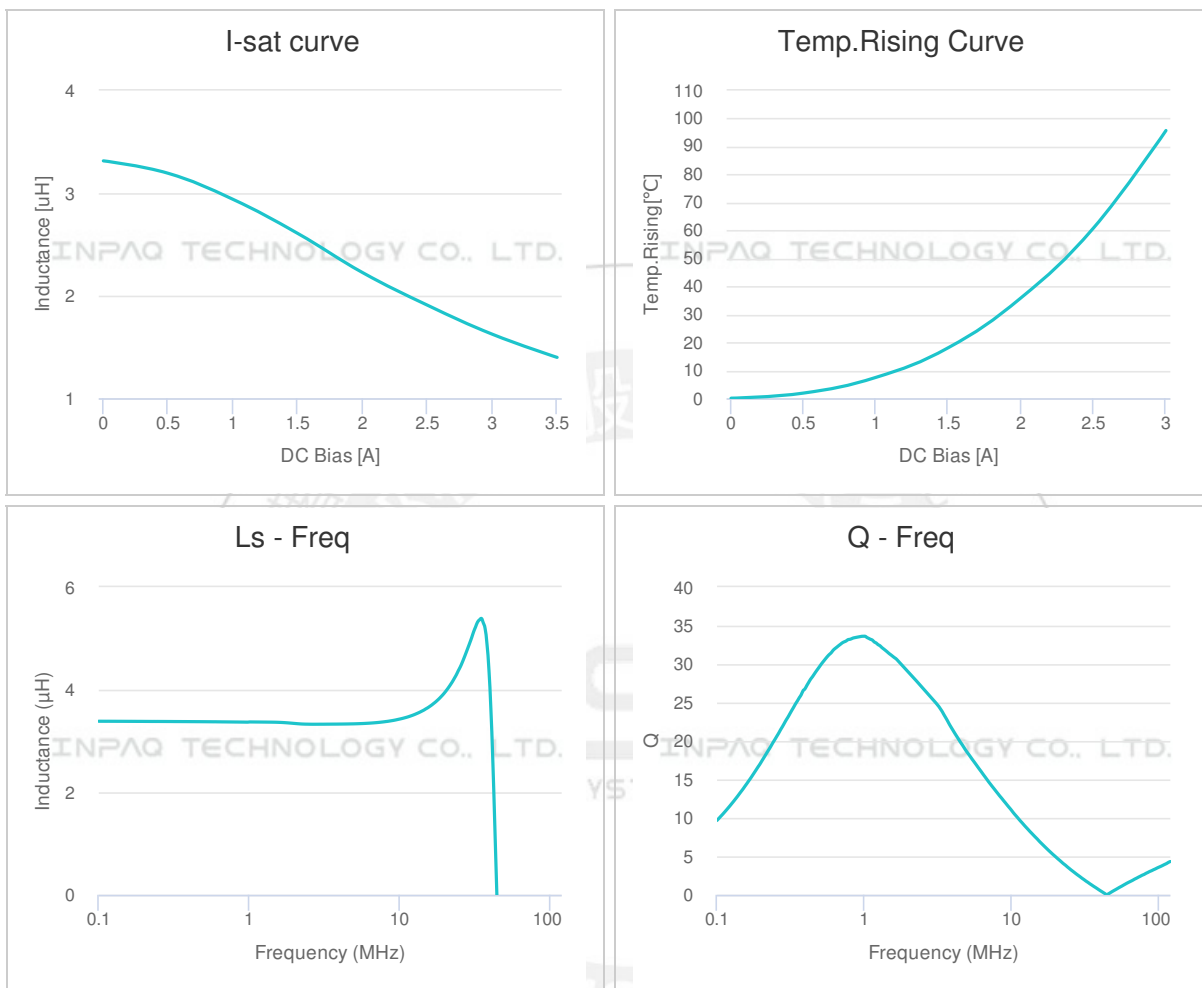
Packing Reel(Inch)	Standard package quantity(KPCS)	Piece weight(gram)	MSL Level
7	3	0.0191	1

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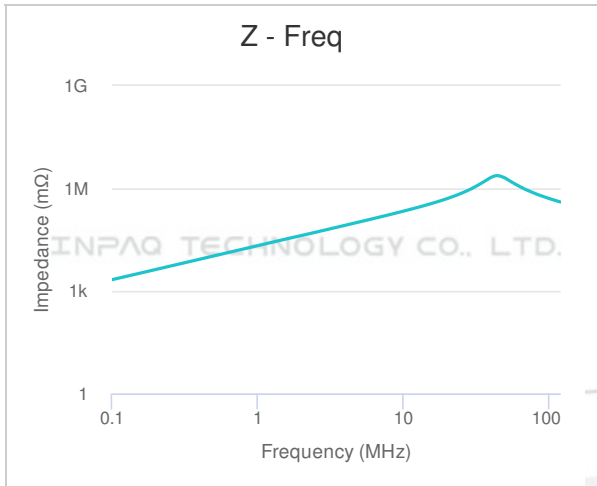
## Characteristic curve



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